IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of		Confirm. No.:	4019
Yasuhiko NAKASHIMA		Atty. Ref.:	1035-660
Serial No.	10/593,695	TC/A.U.:	2183
Filed:	September 20, 2006	Examiner:	Idriss N. ALROBAYE

For: DATA PROCESSING DEVICE FOR IMPLEMENTING INSTRUCTION REUSE, AND DIGITAL DATA STORAGE MEDIUM FOR STORING A DATA PROCESSING PROGRAM FOR IMPLEMENTING INSTRUCTION REUSE

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April 26, 2010

MAIL STOP AF
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

AGENDA FOR INTERVIEW

The undersigned appreciates the scheduling of a telephone Interview for the above identified application on Monday, April 26, 2010, at 3:30 PM. The telephone call will be initiated by Mr. Niessen.

Interview Date: Monday April 26, 2010
Proposed Interview Time: 3:30 PM
Location: n/a: telephonic
Participants: Examiner Idriss Alrobaye
Supervisory Examiner Eddie Chan

AGENDA ITEMS

1. Brief overview of claimed subject matter including but not limited to the use of dependency relations information.

2. Independent claim 1 requirement of dependency relations information that indicates which input element in an input pattern is associated with each output element in the derived output pattern.

3. Applicant's contention that the term "data dependency" in the Miki '474 reference implies that a following instruction in the program must wait for the termination of execution of the preceding instruction (see col. 1, lines 16-25) and the past execution result of the instruction is stored in a memory (col. 2, lines 27-32) – which is different from Applicant's claimed "dependency relations information" which indicates the particular input element in the input pattern from which each output element is derived.

4. Applicants' contention that the term "data dependencies" in the Huang '909 reference implies that the processing of any element (17) can not be completed until the processing in adjacent elements on both sides is completed (see, e.g., col. 5, lines 5-15) but since the Huang '909 reference only broadly discloses a connection relationship between input elements of the input pattern and output elements of the output pattern for the time of execution of one or more program instructions in the instruction region, it does not teach or suggest the specific limitations of Applicant's claimed "dependency relations information" which indicates the particular input element in the input pattern from which each output element is derived, as set forth in Applicant's claim 1.

5. Discussion of proposed further amendments to independent claim 1 to more clearly specify that the generated dependency relations information includes information that indicates the particular *input value and input address* of a register/memory from which the readout is performed that corresponds to *each output value and output address* of the associated output pattern.

Respectfully submitted,

NIXON & VANDERHYE P.C.

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